



MANGALORE INSTITUTE OF TECHNOLOGY & ENGINEERING

(A unit of Rajalaxmi Education Trust ®, Mangalore)

Autonomous Institute Affiliated to V.T.U., Belagavi , Approved by AICTE, New Delhi

Accredited by NAAC with A+ Grade & ISO 9001:2015 Certified Institution

MITE-KSCST IPR Cell

Academic Year 2025-26

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IP Statistics

No of Patent Applications Filed	No of Patent Applications Published	No of Patent / Design Granted
64	50	6



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MITE-KSCST IPR Cell

Members for the Academic Year 2025-26

The following faculty are the members of the reconstituted committee for the Academic year 2025-26

S.No	Name and Designation	Role
1	Dr. Prashanth C M Principal	Chairperson
2	Dr. Ramalingam H M , Associate Professor, Dept. of Electronics & Communication Engineering	Convener
3	Dr. Vignesh Nayak Ullal , Sr. Assistant Professor, Dept. of Mechanical Engineering	Member
4	Dr. Rejeesh Rayaroth , Assistant Professor, Dept. of Computer Science & Engineering	Member
5	Dr. Sreeja Rajesh , Associate Professor, Dept. of Information Science & Engineering	Member
6	Mr. Vinod Kumar , Assistant Professor, Dept. of Mechatronics Engineering	Member
7	Dr. Anushree Raj , Senior Assistant Professor, Department of MCA	Member
9	Mr. Ranjith H D , Sr. Assistant Professor, Dept. of Electronics & Communication Engineering	Member
10	Mr. Sandeep S Naik , Assistant Professor, Department of CSE (Internet of Things & Cyber security with Blockchain Technology)	Member
11	Mr. Amar Gandge Subhash , Assistant Professor, Department of Aeronautical Engineering	Member
12	Dr. Yogeswara Reddy P , Assistant Professor, Dept. of Artificial Intelligence and machine Learning	Member



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MITE- KSCST IPRCELL

Training Session on VTU Consortium, EBSCO & IEEE Digital Resources

Date: February 11th 2026

Resource Person:
Mr. M.S. Srinivasa,
Training Manager, EBSCO

Where Stories Turn into Diamonds

National Digital Library of India

IEEE MITE STUDENT BRANCH

MITE-KSCST IPR CELL

EBSCO

Organizing

Training Session on EBSCO & IEEE Digital Resources

Resource Person:
Mr. M S Srinivasa
Training Manager
EBSCO

Date: February 11, 2026 **Time: 2 PM**

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A comprehensive training session on VTU Consortium, EBSCO & IEEE Digital Resources was successfully conducted on 11th February 2026 at Mangalore Institute of Technology & Engineering (MITE). The program aimed to create awareness and provide hands-on training to faculty members, research scholars, and students regarding the effective utilization of premium digital research resources.

The session commenced with a warm welcome address by Dr. Santhosh Kumar T V, Librarian, MITE, who formally welcomed the Chief Guest, Mr. M. S. Srinivasa, Training Manager – EBSCO, along with faculty members and participants. He emphasized the importance of digital research databases in enhancing academic excellence, research productivity, and innovation within the institution.

The training program focused on:

- Overview of VTU Consortium digital resources
- Access and utilization of EBSCO Engineering Suite
- Effective usage of IEEE digital resources
- Demonstration of advanced search strategies
- Introduction to AI-enabled research tools for optimized literature search
- Awareness about AMA (American Management Association) MicroCourses and certification opportunities

The EBSCO Engineering Suite provides extensive access to eJournals and eBooks covering circuit branches and core engineering disciplines, along with comprehensive resources for the MBA department. With access to over 3,900 Web of Science (WoS) and Scopus-indexed journals, the platform offers high-quality, peer-reviewed content supporting research and academic projects.



Dr. Santhos Kumar T V, Librarian Welcomming the Guset

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Mr. M.S. Srinivasa Explaining the access of Various courses

The IEEE digital resources session highlighted access to globally recognized technical papers, standards, and conference proceedings essential for engineering and technology domains. The demonstration included structured search techniques, citation tracking, and content filtering for efficient research output.

A special highlight of the session was the introduction to AI-powered research tools, which streamline academic workflows and enhance the accuracy and speed of literature reviews. Additionally, the AMA MicroCourses—designed by experts from the American Management Association—were presented as valuable upskilling opportunities for faculty, students, Heads of Departments, and academic leaders. These micro-courses provide industry-relevant knowledge and prestigious e-certificates that strengthen professional profiles.

The training witnessed enthusiastic participation from approximately 600 attendees, including faculty members, research scholars, and students from various departments, reflecting the institution's strong commitment to research and academic development. Following the presentation, Mr. M. S. Srinivasa organized a quiz competition for the participants and awarded prizes to the winners. At the conclusion of the session, Prof. Narendra U P, Director (Placement,

Training & Industry Relations) facilitated the Chief Guest as a token of appreciation for delivering an insightful and impactful session.

The program formally concluded with a Vote of Thanks proposed by Dr. Ramalingam H M, Convener – MITE KSCST IPR Cell, who expressed sincere gratitude to the Registrar, VTU Belagavi, VTU Consortium, and VTU Consortium coordinator Dr. Somaraya. B Tallolli, the resource person, management, organizing team, and all participants for making the program a grand success.



During the Session Speaker conducted quiz

The training session proved to be highly informative and transformative, significantly enhancing awareness and effective utilization of digital research resources among the academic community at MITE.

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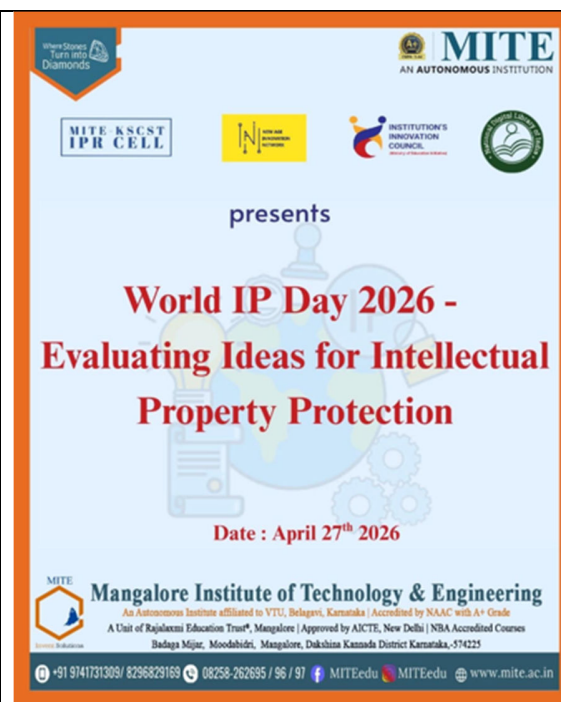
Prof. Narendra U. P., Director – Training & Placements, presenting a memento to the guest speaker.

World IP Day 2026: Training on Prior Art Search and Patent Application Evaluation

Date: February 27th April 2026

Resource Person:

Dr. Ramalingam H M,
Convener, MITE-KSCST IPR
Cell



As part of the World Intellectual Property Day 2026 celebrations, a training programme on “Prior Art Search and Evaluation of Inventions for Patent Application” was organized by the MITE–KSCST IPR Cell on 27 April 2026 at the Innovation Center, MITE. The programme was conducted for the shortlisted students of Ideathon 2026.

The main objective of the programme was to help students understand how to check whether their innovative ideas are new and suitable for patent protection. The session gave students a practical understanding of prior art search, which is an important step before applying for a patent.

Dr. Ramalingam H M, Convener, MITE–KSCST IPR Cell, was the resource person for the session. He explained the process of searching existing patents, research publications, and other available information related to an invention. He also explained how students can compare their ideas with existing technologies and identify the novel aspects of their inventions.



Dr.Ramalingam H M during his session on Patent process

The session also covered the basic aspects of **novelty, inventive step, and patentability**. The students were guided on how to evaluate their Ideathon projects from a patent perspective and understand whether their ideas have sufficient uniqueness for intellectual property protection.

The programme was interactive, and students actively participated in the session and discussed their ideas and queries with the resource person. The practical guidance provided during the programme helped the students gain better clarity on how to proceed with their innovative ideas and patent applications.



Interacting with the students regarding the Ideas shortlisted

Overall, the training programme was useful for the shortlisted **Ideathon 2026** students and helped create awareness about the importance of **prior art search and intellectual property protection**. The programme encouraged students to further develop their innovative ideas and explore suitable opportunities for protecting them through patents.

From Idea to IP: Patents, Protection, Valuation, and CRI for Academia and Industry”

Resource Person



Dr. Dasharathraj K Shetty
Associate Professor
School of Computer Engineering
MIT Manipal

Department Heads

Dr. Ravinarayana B
Professor & Head
Dept. of Computer
Science & Engineering

Mr. Manjunath H
Professor & Head
Dept. of Information
Science & Engineering

Convenors

Dr. Sreeja Rajesh.
Associate Professor
Information Science &
Engineering

Dr. Rejeesh Rayaroth
Assistant Professor
Computer Science &
Engineering



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**MITE-KSCST
IPR CELL**



**Departments of Computer Science & Engineering
&
Information Science & Engineering
in association with**



MITE-KSCST IPR Cell
is organizing



**Workshop
on**

**“From Idea to IP: Patents, Protection,
Valuation, and CRI for Academia and Industry”**



08-09 MAY 2026



ADE Lab, Main Block

Day: 1

The Department of Computer Science Engineering and Information Science Engineering jointly **organized** a workshop titled “From Idea to IP: Patents, Protection, Valuation, and CRI for Academia and Industry.” This workshop **aimed** to enhance awareness and practical understanding of intellectual property (IP) among student, faculty and researchers. It **covered** key aspects of identifying patentable ideas, strategies for protecting innovations, basics of IP valuation, and computer-related inventions (CRI) with a focus on both academia and industry. The workshop was conducted with the approval and support of IQAC and aimed at enhancing students’ practical knowledge in modern full-stack web development technologies.

The inaugural session of the workshop commenced on 8th May 2026 in the presence of faculty members, student participants, and organizing committee members. The workshop was coordinated under the guidance of the Heads of the Departments, **Dr. Ravinarayana B** and **Prof. Manjunath H**, and the convenor of the IPR Cell, **Dr. Ramalingam, HM** and was organized by **Dr. Sreeja Rajesh** and **Dr. Rejeesh Rayaroth** from the Departments of IS&E and CS&E, respectively.



During the Inaugural of the Event

The resource person for the workshop was Dr. Dasharathraj K Shetty, Associate Professor, School of Computer Engineering, MIT Manipal. The sessions introduced the participants to the importance of intellectual property (IP) in academia and industry and highlighted how different forms of IP protection support innovation and commercialization.

On the first day, the workshop primarily focused on the fundamentals of IP and patenting. The sessions included:

Session 1: Introduction and Importance of IP in Academia and Industry

- Overview of different types of Intellectual Property Rights (IPR): patents, copyrights, trademarks, industrial designs, and geographical indications.
- Role of IP in promoting research, innovation, and technology transfer in academic institutions.
- Why faculty and students should protect their innovations: safeguarding ideas, recognition, and potential commercialization.

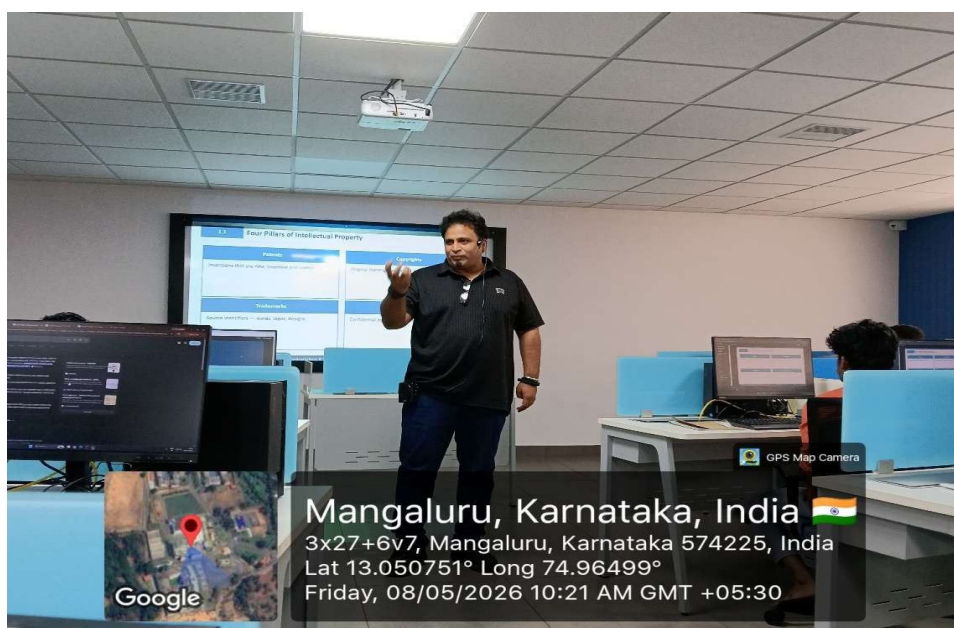
- Examples of IP generated from academic projects, publications, and collaborations with industry.

Session 2: Patents and the Patent Filing Process

Day 1 – Session 2: Patents and the Patent Filing Process

- What is a patent: definition, scope of protection, and key terminology (invention, claims, prior art, etc.).
- Patentability criteria: novelty, inventive step, and industrial applicability, with simple illustrative examples.
- Basics of prior art search: why it is done and the importance of checking existing patents and publications.
- Overview of the Indian patent filing process:
 - Provisional and complete specifications
 - Major forms and steps (filing, examination, publication, grant in brief)

The resource person blended foundational concepts with practical activities, including a patent search primer, India-focused filing steps, IP value extraction, and a clear walkthrough of CRI guidelines to demystify software-related inventions and help participants understand how to frame disclosures and claims effectively.



Dr. Dasharathraj during his session

The interactive nature of the sessions encouraged participants to ask questions and clarify their doubts regarding intellectual property protection, patent filing, and the relevance of IP in academic and industrial research.

The workshop witnessed enthusiastic participation from students and of the Computer Science & Engineering and Information Science & Engineering departments. The sessions were highly informative and application-oriented, helping participants gain a clearer understanding of IP frameworks, patentability criteria, and Indian patent filing procedures.

By the end of Day 1, participants were able to build strong conceptual clarity on different types of IPR—patents, copyrights, trademarks, designs, and geographical indications—and their relevance in academia and industry, as well as understand the basic requirements for patentability, the role of prior art search, and an overview of the Indian patent filing process.

The first day concluded with an interactive discussion and feedback session, during which participants shared their learning experiences and sought further guidance on protecting their innovations through appropriate IP mechanisms.



Faculty and Student Participants during the session

Day: 2

The second day of the workshop “From Idea to IP: Patents, Protection, Valuation, and CRI for Academia and Industry” continued with concept-driven and practice-oriented sessions on innovation, IP value extraction, and computer-related inventions (CRI). The sessions were handled by **Dr. Dasharathraj K Shetty**, who guided participants through the process of converting innovative ideas into protectable IP and understanding the specific guidelines related to software- and computer-related patents.

The day began with a brief recap of the core IP and patenting concepts covered on Day 1, followed by advanced topics including:

- **Innovation and Idea to IP Conversion**
- **Identifying patentable features in academic and industry projects**
- **Basics of IP value extraction and commercialization potential**
- **Introduction to CRI and the Indian CRI examination guidelines**
- **Distinguishing patentable and non-patentable computer-related inventions**
- **Drafting disclosures and understanding claim-framing basics for CRI**

The resource person conducted detailed demonstrations and guided exercises illustrating how to map research outputs and project ideas to different forms of IP, and how to assess their protectability and potential value. Participants were encouraged to work individually and in small groups to analyse sample problem statements, identify inventive elements, and relate them to patentability criteria and CRI norms.

The workshop on Day 2 mainly emphasized hands-on analytical practice, where participants engaged in activities such as basic prior-art search demonstrations, drafting brief invention disclosures, and discussing possible IP strategies for academic projects and software-based solutions. Through these activities, they gained practical exposure to:

- **Identifying innovative components within technical projects**
- **Relating technical features to IP protection routes**
- **Understanding how CRI guidelines impact software and algorithm-based inventions**
- **Thinking about commercialization and value extraction at an early research stage**

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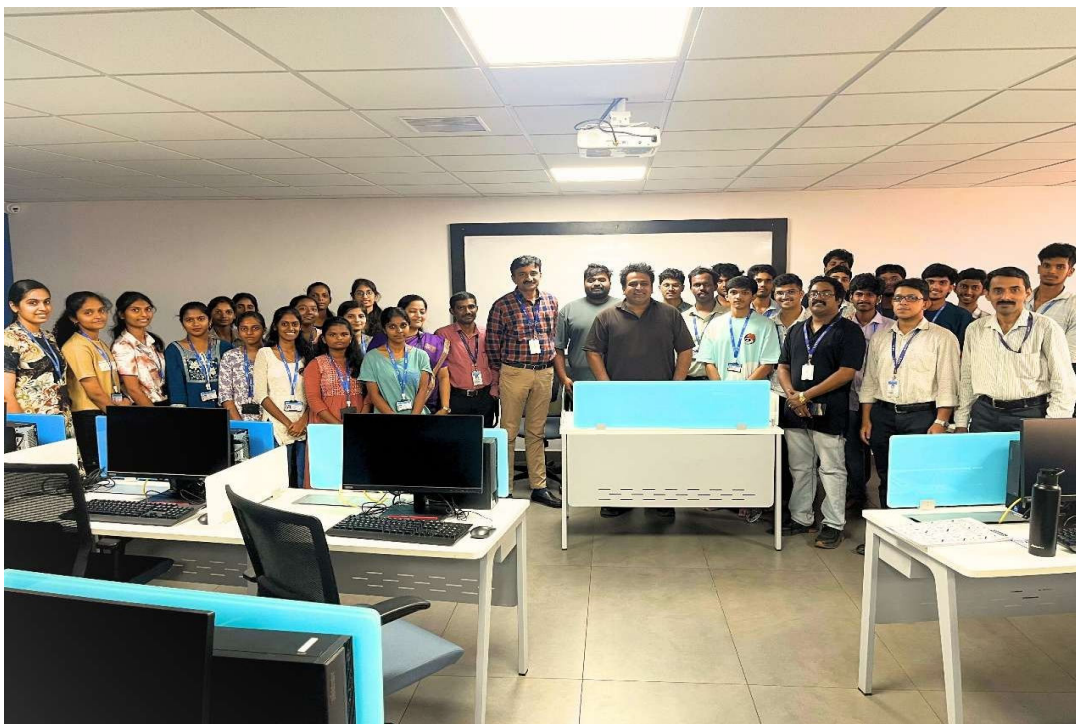
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The interactive sessions greatly enhanced the participants' confidence in dealing with IP-related aspects of their academic and collaborative work. The resource person also shared valuable insights on:

- **The role of IP in research careers and industry collaborations**
- **Best practices for documenting innovations and maintaining lab/project records**
- **Common pitfalls in patent filing and CRI-related applications**
- **The importance of aligning research planning with IP and commercialization possibilities**

Participants actively engaged throughout the sessions and clarified various queries related to patentability, CRI interpretation, publication versus patenting, and institutional IP support mechanisms.

By the end of Day 2, participants were able to identify innovative ideas suitable for IP protection, perform basic assessment of their IP value, and understand and interpret CRI guidelines to distinguish patentable computer-related inventions while framing preliminary disclosures and claims in line with Indian practice.



Participants of the Workshop in the valedictory session



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The valedictory session was conducted at the end of the workshop. The organizing team expressed sincere gratitude to the Management of MITE, the Principal of MITE, the Heads of the Departments, **Dr. Ravinarayana B** and **Mr. Manjunath H**, the convenor IPR cell MITE **Dr. Ramalingam HM**, the IQAC for its approval and support, the resource person **Dr. Dasharathraj K Shetty**, and all faculty coordinators and student volunteers for their contributions to the successful conduct of the workshop